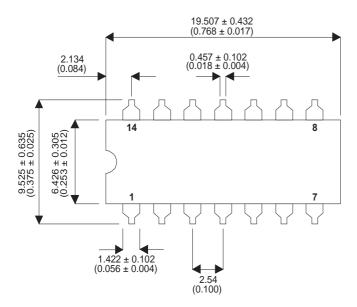


# **SMLG110**

### **MECHANICAL DATA**

Dimensions in mm (inches)



#### 14 LEAD MOULDED DIP PACKAGE

#### N-CHANNEL N-CHANNEL N-CHANNEL

1—Drain 1 5—Gate 2 8—Drain 3 12—Gate 4 2—Source 1 6—Source 2 9—Source3 13—Source 4 3—Gate 1 7—Drain 2 10—Gate 3 14—Drain 4

11,4-NC

# 14 LEAD DUAL IN LINE QUAD N-CHANNEL POWER MOSFETS

BV<sub>DSS</sub> ±100V

I<sub>D(cont)</sub> 1A

 $R_{DS(on)}$  0.7 $\Omega$ 

## **FEATURES**

- AVALANCHE ENERGY RATED
- HERMETICALLY SEALED
- DYNAMIC dv/dt RATING
- SIMPLE DRIVE REQUIREMENTS
- FOR AUTOMATIC INSERTION
- SIMPLE DRIVE REQUIREMENTS
- EASE OF PARALLELING
- 4 N-CHANNEL CO-PACKAGED HEXFETS
- LIGHTWEIGHT

# **ABSOLUTE MAXIMUM RATINGS**(T<sub>case</sub> = 25°C unless otherwise stated)

$\overline{V_{GS}}$	Gate – Source Voltage	±20V		
$I_{D}$	Continuous Drain Current (V <sub>GS</sub> = 10V , T <sub>case</sub> = 25°C)	1.A		
$I_{D}$	Continuous Drain Current (V <sub>GS</sub> = 10V , T <sub>case</sub> = 100°C)	0.6A		
$I_{DM}$	Pulsed Drain Current	4A		
$P_{D}$	Power Dissipation @ T <sub>case</sub> = 25°C	1.4W		
	Linear Derating Factor	0.011W/°C		
E <sub>AS</sub>	Single Pulse Avalanche Energy <sup>2</sup>	75mJ		
dv/dt	Peak Diode Recovery <sup>3</sup>	5.5V/ns		
$T_J$ , $T_stg$	Operating and Storage Temperature Range	−55 to 150°C		
$R_{ heta JC}$	Thermal Resistance Junction to Case	6.25°C/W		
$R_{\theta JCA}$	Thermal Resistance Junction-to-Ambient	175°C/W		

#### Notes

- 1) Pulse Test: Pulse Width  $\leq 300 \mu s$ ,  $\delta \leq 2\%$
- 2) @  $V_{DD}$  = 25V ,  $L \geq$  112mH ,  $R_G$  = 25 $\Omega$  , Peak  $I_L$  = 1A , Starting  $T_J$  = 25°C

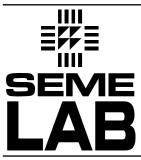
E-mail: sales@semelab.co.uk

3) @ I\_{SD}  $\leq$  1A , di/dt  $\leq$  75A/µs , V\_DD  $\leq$  BV\_DSS , T\_J  $\leq$  150°C , Suggested R\_G = 24 $\Omega$ 

Semelab Plc reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.

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# **SMLG110**

# **ELECTRICAL CHARACTERISTICS** (T<sub>amb</sub> = 25°C unless otherwise stated)

	Parameter	Test Conditions		Min.	Тур.	Max.	Unit	
	STATIC ELECTRICAL RATINGS							
BV <sub>DSS</sub>	Drain – Source Breakdown Voltage	V <sub>GS</sub> = 0	I <sub>D</sub> = 1mA	100			V	
$\Delta BV_{DSS}$	Temperature Coefficient of	Reference to 25°C			0.40		1,,,,,	
$\Delta T_{J}$	Breakdown Voltage	I <sub>D</sub> = 1mA			0.13		V/°C	
R <sub>DS(on)</sub>	Static Drain – Source On–State	V <sub>GS</sub> = 10V	I <sub>D</sub> = 0.6A			0.70		
	Resistance	V <sub>GS</sub> = 10V	I <sub>D</sub> = 1A			0.80	Ω	
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}$	I <sub>D</sub> = 250μA	2		4	V	
9 <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> ≥ 15V	I <sub>DS</sub> = 0.60A	0.86			S(\Omega)	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{GS} = 0$	$V_{DS} = 0.8V_{DSS}$ $T_{J} = 125^{\circ}C$			25 250	μΑ	
I <sub>GSS</sub>	Forward Gate – Source Leakage	V <sub>GS</sub> = 20V	-5			100		
I <sub>GSS</sub>	Reverse Gate – Source Leakage	$V_{GS} = -20V$				-100	—— nA	
	DYNAMIC CHARACTERISTICS							
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0			180			
C <sub>oss</sub>	Output Capacitance	$V_{DS} = 25V$			82		pF	
C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1MHz			15		-	
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> = 10V	I <sub>D</sub> = 1A			15		
Q <sub>gs</sub>	Gate - Source Charge	$V_{DS} = 0.5V_{DS}$				7.5	nC	
Q <sub>gd</sub>	Gate - Drain ("Miller") Charge					7.5		
t <sub>d(on)</sub>	Turn-On Delay Time					20		
t <sub>r</sub>	Rise Time	$V_{DD} = 50V$			25	- ns		
t <sub>d(off)</sub>	Turn-Off Delay Time	I <sub>D</sub> = 1A			40			
t <sub>f</sub>	Fall Time	$R_G = 24\Omega$				40		
	SOURCE - DRAIN DIODE CHARAC	TERISTICS					.1	
I <sub>S</sub>	Continuous Source Current					1	Λ	
I <sub>SM</sub>	Pulse Source Current <sup>2</sup>					4	A	
$V_{SD}$	Diode Forward Voltage <sup>1</sup>	$I_S = 1.0A$ $V_{GS} = 0$	T <sub>J</sub> = 25°C			1.5	V	
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> = 1A	T <sub>J</sub> = 25°C			200	ns	
Q <sub>rr</sub>	Reverse Recovery Charge	d <sub>i</sub> / d <sub>t</sub> ≤ 100A/μs	s V <sub>DD</sub> ≤ 50V			0.83	μС	
t <sub>on</sub>	Forward Turn-On Time				Negligible			
	PACKAGE CHARACTERISTICS							
L <sub>D</sub>	Internal Drain Inductance (from centre of drain pad to die)				4.0			
L <sub>S</sub>	Internal Source Inductance (from centre	of source pad to end		6.0		⊣ nH		

## **Notes**

- 1) Pulse Test: Pulse Width  $\leq 300 \mu s$ ,  $\delta \leq 2\%$
- 2) Repetitive Rating Pulse width limited by maximum junction temperature.

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